

ABSTRACT OF THE DISCLOSURE

5 A thin film transistor array panel is provided, which includes: a gate line and a data line formed on an insulating substrate and intersecting each other; a plurality of common electrodes separated from the gate line and the data line and making an angle of about 7-23 degrees with the gate line; a plurality of pixel electrodes separated from the gate line, the data line, and the common electrodes, extending parallel to the common electrodes, and alternately arranged with the common electrodes; and a thin film transistor connected to the gate line, the data line, and the pixel electrodes.